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Docket: 0756-0935

\$ 20<sup>0</sup> 126 8/1/95  
#10/bgt. 11/14/95

Rose M. S. G.



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re PATENT Application of: ) OCT 31 1995  
Shunpei YAMAZAKI et al. )  
Serial No. 08/160,909 ) Art Unit: 1107  
Filed: December 3, 1993 ) Examiner: M. Wilczewski  
For: METHOD OF FABRICATING )  
SEMICONDUCTOR DEVICES )  
AND APPARATUS FOR )  
PROCESSING A )  
SEMICONDUCTOR )

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents and Trademarks

Washington, D.C. 2023

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant directs the attention of the Examiner to related U.S. patent application Serial No. 08/275,909, filed July 15, 1994, titled *Method of Annealing a Semiconductor*. Submitted herewith on an attached Form PTO-1449 are references of record in this related application and Applicant requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

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A duplicate copy of this sheet is attached. A \$220.00 fee in accordance with 37 C.F.R. §1.17(p) is attached.

Respectfully submitted,

  
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